

Topology For Providing Clock Signals To Multiple Circuit Units On A Circuit Module

ABSTRACT

A circuit module has a circuit board, multiple circuit units on the circuit board and at least one clock input on the circuit board for receiving an external clock signal. The circuit module has a first PLL unit on the circuit board for providing an internal clock signal based on the external clock signal to at least a first one of the circuit units. In addition, the circuit module has a second PLL unit on the circuit board for providing an internal clock signal based on the external clock signal to at least a second one of the circuit units.